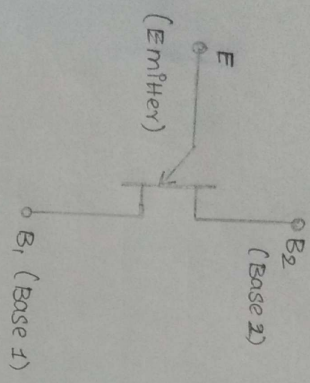
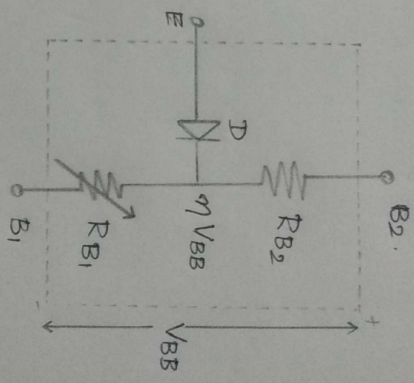


UNI JUNCTION TRANSISTOR
 ↓
 current controlled negative resistance device.

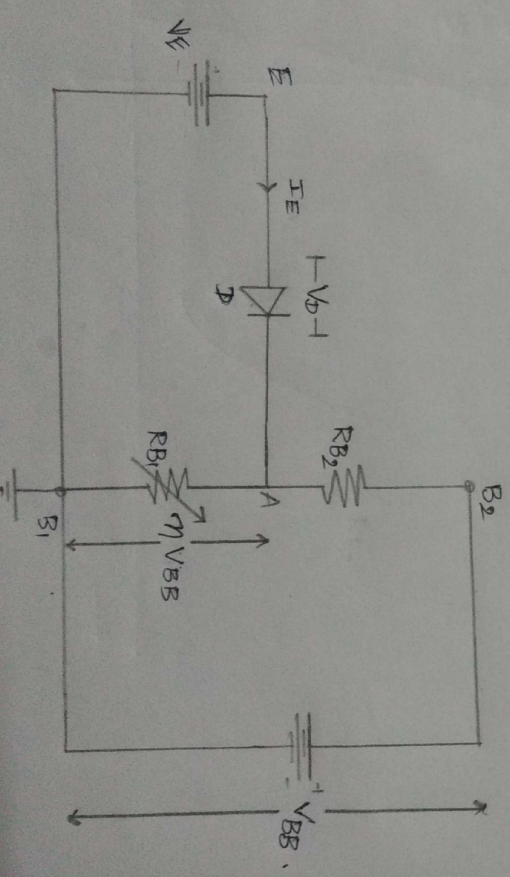
SYMBOL



SIMPLIFIED EQUIVALENT CIRCUIT



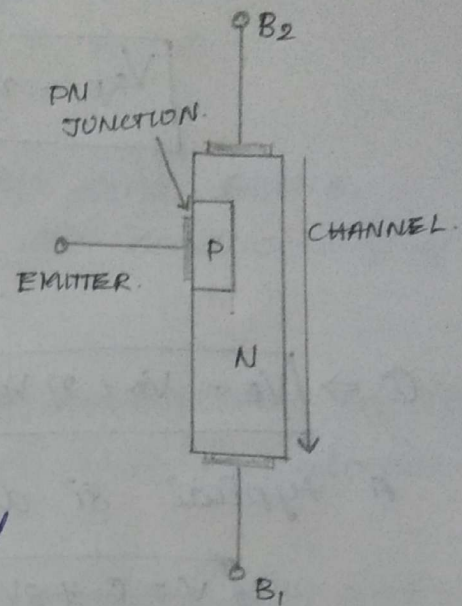
CIRCUIT DIAGRAM:-



Construction:-

• Has 3 terminals \Rightarrow an emitter, & Bases B_1 and B_2 . Since it consists of two bases, it is also called double base diode.

• Base is Formed of lightly doped n-type base of silicon. & ohmic contacts B_1 and B_2 are attached to its ends.



• Emitter is Formed of Heavily doped p-type material.

Emitter is located closer to B_2 than B_1 . Resistance between Base 1 and Base 2 when Emitter Junction is open is called Inter base Resistance.

Working:-

- When no potential exists b/w emitter and either of its base, extremely small current flows from Base 1 to Base 2.
- DTD works effectively when the Emitter-Base Junction is Forward biased.
- The Emitter Base Junction is Forward biased only when the applied potential (V_E) is greater than the sum of the barrier potential of the diode (V_D) and voltage drop across R_{B1} .

$$\text{i.e. } \boxed{V_E = V_D + V_{RB1}} \rightarrow \textcircled{1}$$

By Applying voltage division Rule, in the circuit

$$V_{RB_1} = \frac{V_{BB}}{R_{B_2} + R_{B_1}} \cdot R_{B_1}$$

$$\boxed{V_{RB_1} = \eta V_{BB}}$$
 where $\eta = \frac{R_{B_1}}{R_{B_2} + R_{B_1}}$

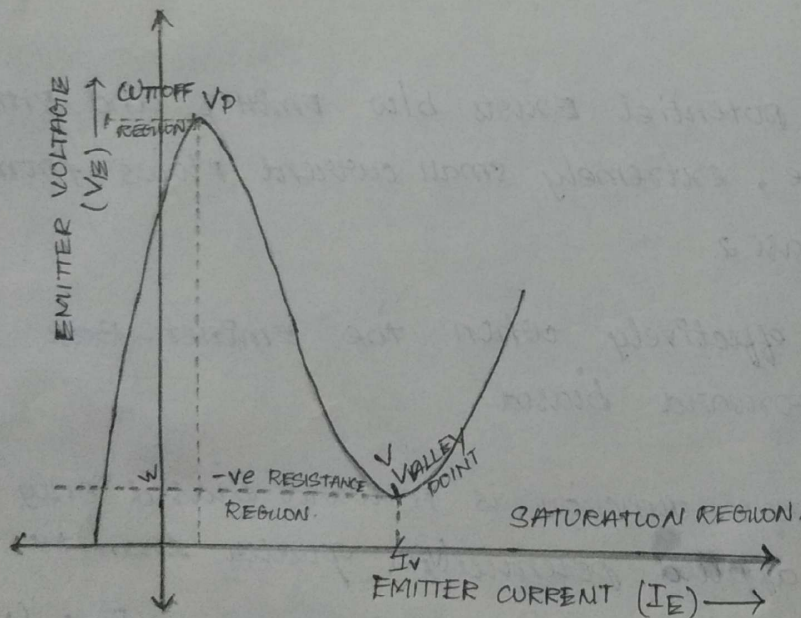
η - intrinsic stand-off Ratio.
 typical Range \rightarrow (0.4 to 0.8)

$$\textcircled{1} \Rightarrow \boxed{V_E = V_D + \eta V_{BB}}$$

A typical SP diode has $V_D = 0.7 \text{ eV}$.

$$\boxed{V = 0.7 \text{ eV} + \eta V_{BB}}$$

Thus, the minimum value of emitter voltage V_E for which the emitter starts to flow is called Triggering / Firing voltage of UJT.



• AS V_E increases, emitter current (I_E) also will increase and here the junction behaves like a typical PN junction.

But V_E can be increased upto particular voltage called peak voltage (V_D).

At this point considerable amount of emitted current flows and significant number of holes are injected into the junction. These holes are repelled by B_2 and attracted by B_1 .

- As a result, region b/w emitter and base B_1 starts saturating by holes and the conductivity of this region starts to increase

This phenomenon of increase in conductivity by the injection of holes is called conductivity modulation.

- This increase in conductivity, reduces R_{B_1} and η .

This results in a condition where I_E ~~increases~~ increases and V_E decreases. and this situation results in negative-resistance region.

- The negative ^{resistance} region of curve lasts until the valley point V is reached with the valley voltage (V_V) and valley point current I_V .

After the valley point, the device is driven to saturation.

Applications :-

- 1) Triggering device for SCR's and TRIAC's.
- 2) Sawtoothed generators, simple oscillators, phase control and timing circuits.
- 3, simplest of all UJT is Relaxation oscillator producing non-sinusoidal waveforms.